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TRANSMITTAL FORM (To be used for all correspondence after initial filing)	Application Number	10/667,199
	Filing Date	September 18, 2003
	First Named Inventor	Ashish Kumar Goel
	Art Unit	2184
	Examiner Name	
	Attorney Docket No.	852463.403

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement; Form PTO-1449 <input checked="" type="checkbox"/> Cited References <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts under 37 C.F.R. 1.52 or 1.53 <input type="checkbox"/> Response to Missing Parts/Incomplete Application	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Request for Corrected Filing Receipt <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation, Change of Correspondence Address <input type="checkbox"/> Declaration <input type="checkbox"/> Statement under 37 CFR 3.73(b) <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund	<input type="checkbox"/> CD(s), Number of CD(s) _____ <input type="checkbox"/> After Allowance Communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Return Receipt Postcard <input type="checkbox"/> Additional Enclosure(s) (please identify below): _____ _____ _____
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Individual Name	Timothy L. Boller	Customer Number 38106
Signature		
Date	July 12, 2004	

CERTIFICATE OF TRANSMISSION/MAILING		
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.		
Typed or printed name	Jason Anover	
Signature		Date: July 12, 2004

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. 497307



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July 12, 2004
Date

Jason Añover

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Ashish Kumar Goel et al.
Application No. : 10/667,199
Filed : September 18, 2003
For : METHOD AND APPARATUS OF RELOADING ERRONEOUS
CONFIGURATION DATA FRAMES DURING CONFIGURATION
OF PROGRAMMABLE LOGIC DEVICES

Art Unit : 2184
Docket No. : 852463.403
Date : July 12, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 CFR 1.56 and 1.97 through 1.98, applicants wish to make known to the U.S. Patent and Trademark Office the references set forth on the attached Form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 CFR 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed

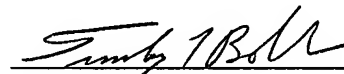
relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Director is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC



Timothy L. Boller

Registration No. 47,435

Enclosures:

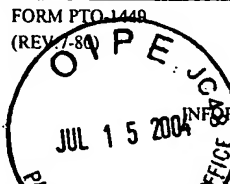
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Form PTO-1449

Cited References (4)

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FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 852463.403	APPLICATION NO. 10/667,199
 INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANTS Ashish Kumar Goel et al.	
		FILING DATE September 18, 2003	GROUP ART UNIT 2184

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
				YES	NO
AJ					
AK					
AL					
AM					

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AN	ALTERA's Application Note 116 "Configuring APEX 20K, FLEX 10K, FLEX 6000 Devices," pp. 1-77, May 2000.
AO	ALTERA's Application Note 33 "Configuring FLEX 8000 devices," pp. 33-71, June 2000.
AP	Xilinx's Application Note XAPP138 "Virtex FPGA Series Configuration and Readback," pp. 1-39, July 11, 2002.
AQ	Xilinx Inc., <i>The programmable Logic databook 1999</i> , May 14, 1999, version 1.6, "XC4000E and XC4000X Series Field Programmable Gate Arrays," pp. 6-49 – 6-72.

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).